

FIG. 1
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A block diagram of a computer system architecture. A central vertical bus, labeled 61 at its bottom, connects several components. On the left side of the bus, from top to bottom, are: a stack of three CPU blocks (the top one labeled 62), a RAM block (labeled 63), a ROM block (labeled 64), an I/O ADAPTER block (labeled 65), and a COMMUNICATION ADAPTER block (labeled 66). On the right side of the bus, from top to bottom, are: a DISPLAY ADAPTER block (labeled 68), a USER INTERFACE ADAPTER block (labeled 67), and a Mouse block (labeled 74). The DISPLAY ADAPTER is connected to a Monitor block (labeled 75). The USER INTERFACE ADAPTER is connected to a Keyboard block (labeled 73). At the bottom left, a Printer block (labeled 70) and a disk drive block (labeled 71) are connected to the I/O ADAPTER. A bidirectional arrow, labeled 72, is connected to the COMMUNICATION ADAPTER. The number 60 is located at the bottom right of the diagram.

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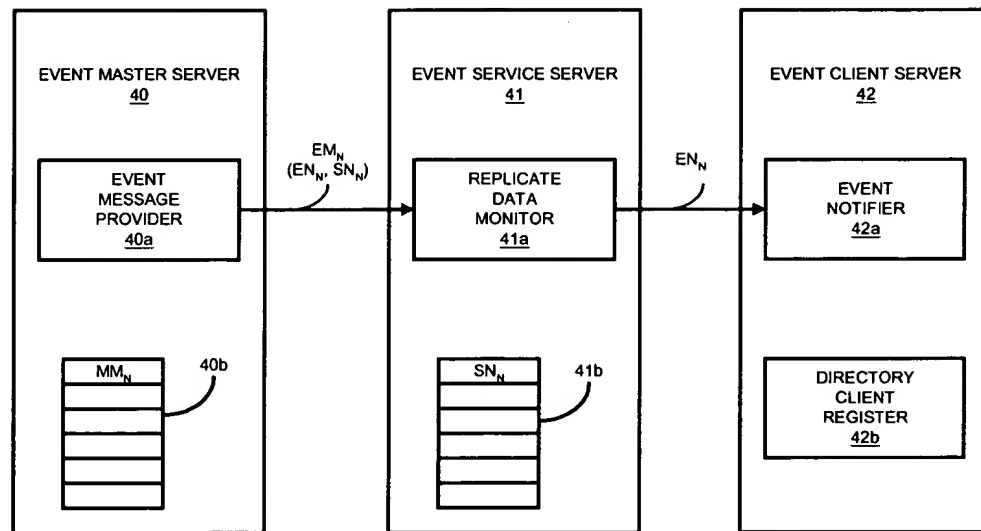


FIG. 2

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0573363 131500

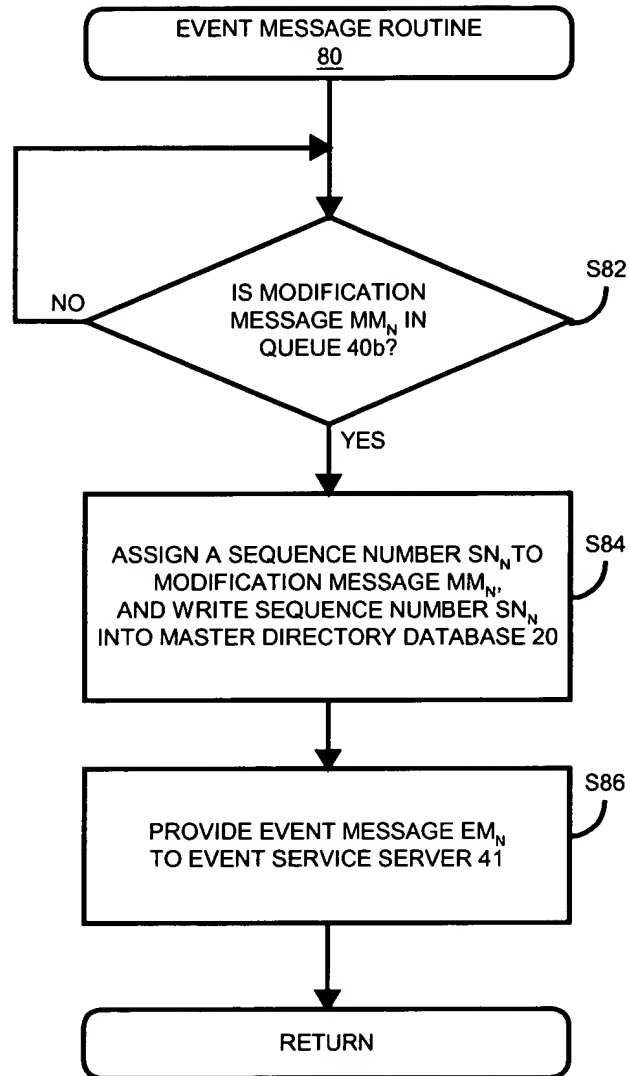


FIG. 3A
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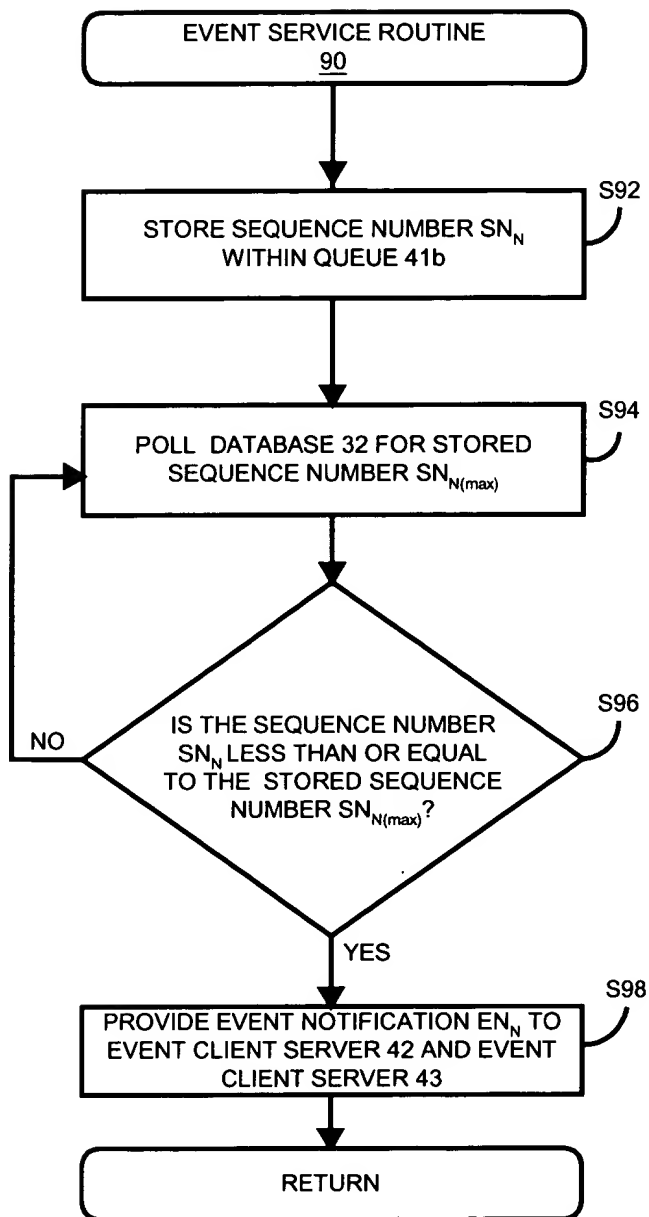


FIG. 3B
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